

IN THE CLAIMS

Please add new claims 125-132.

1. (original) A method for making a spatial light modulator, comprising:
 - providing a first wafer;
 - providing a second wafer;
 - forming circuitry and a plurality of electrodes on or in the first wafer;
 - forming a plurality of deflectable elements on or in either the first or second wafer;
 - bonding the first and second wafers together to form a wafer assembly; and
 - separating the wafer assembly into individual wafer assembly dies.
2. (original) The method of claim 1, wherein each die comprises an array of deflectable reflective elements.
3. (original) The method of claim 2, wherein the reflective elements correspond to pixels in a direct-view or projection display.
4. (original) The method of claim 3, wherein the number of reflective elements in each die is from 6,000 to about 6 million.
5. (original) The method of claim 1, wherein the first wafer is an optically transmissive wafer or a wafer having one or more layers that when removed result in an optically transmissive substrate.
6. (original) The method of claim 5, wherein the first wafer is glass, borosilicate, tempered glass, quartz or sapphire.
7. (original) The method of claim 1, wherein the second wafer is a dielectric or semiconductor wafer.

8. (original) The method of claim 7, wherein the second wafer comprises GaAs or silicon.
9. (original) The method of claim 1, wherein the first and second wafers are bonded together with an adhesive.
10. (original) The method of claim 9, wherein the adhesive is an epoxy.
11. (original) The method of claim 10, wherein the epoxy comprises balls or rods of predetermined diameter.
12. (original) The method of claim 1, wherein the wafer assembly is separated into individual dies by scribing and breaking.
13. (original) The method of claim 1, wherein the wafer assembly is tested for abnormalities prior to separation into the individual dies.
14. (original) The method of claim 1, further comprising providing a spacing wafer between the first and second wafers.
15. (original) The method of claim 1, further comprising providing microfabricated spacers on one or both of the first and second wafers prior to bonding.
16. (original) The method of claim 9, wherein the adhesive is dispensed by automated controlled liquid dispensing through a syringe.
17. (original) The method of claim 9, wherein the adhesive is dispensed by screen, offset or roller printing.

18. (original) The method of claim 16, wherein the syringe is moved along X-Y coordinates for dispensing.
19. (original) The method of claim 1, wherein the aligning comprises registration of substrate fiducials on opposite wafers.
20. (original) The method of claim 19, wherein the registration is accomplished with a video camera having lens magnification.
21. (original) The method of claim 7, wherein the second wafer is a glass or quartz wafer.
22. (original) The method of claim 1, wherein the bonding of the wafers comprises the dispensing of a UV or thermal cure epoxy.
23. (original) The method of claim 22, wherein the bonding further comprises application of a force of 10 kg force or more.
24. (original) The method of claim 1, wherein the aligning comprises aligning each deflectable element on the first wafer with at least one electrode on the second wafer.
25. (original) The method of claim 1, wherein the separation of the wafer assembly comprises forming scribes on the first and second wafers.
26. (original) The method of claim 25, wherein the scribes are placed in an offset relationship to each other in at least one direction.
27. (original) The method of claim 25, wherein the separation further comprises breaking the wafer assembly along the scribe lines with a guillotine or fulcrum breaking machine.

28. (original) The method of claim 1, wherein the separation of the wafer assembly comprises sawing partially through each wafer followed by breaking along the sawed lines.
29. (original) The method of claim 1, wherein the sawing is done in the presence of a high-pressure jet of water.
30. (original) The method of claim 1, wherein the bonding comprises applying a sealant near the perimeter of each array on the wafer.
31. (original) The method of claim 30, further comprising applying a sealant around the perimeter of at least one of the wafers.
32. (original) The method of claim 1, wherein the bonding comprises applying an adhesive and spacers, the spacers having a size of from 1 to 100 microns.
33. (original) The method of claim 32, wherein the spacers have a size of from 1 to 20 microns.
34. (original) The method of claim 1, wherein the plurality of deflectable elements are reflective mirror elements and are formed on the second wafer which is a light transmissive wafer, at least with any surface coating removed therefrom.
35. (original) The method of claim 15, wherein the microfabricated spacers comprise an organic material.
36. (original) The method of claim 32, wherein the spacers are glass or plastic spacers.

37. (original) The method of claim 1, wherein the forming of the deflectable element comprises surface or bulk micromachining.
38. (original) The method of claim 1, wherein the plurality of deflectable elements are formed on or in the first wafer.
39. (original) The method of claim 38, wherein the circuitry and plurality of electrodes are formed prior to forming the plurality of deflectable elements, wherein the plurality of deflectable elements are formed above the plurality of electrodes on the first wafer.
40. (original) The method of claim 39, wherein a plurality of light blocking masks are formed on the second wafer.
41. (original) The method of claim 40, wherein when the wafer assembly is singulated into wafer assembly dies, a light blocking mask is disposed on a second wafer portion within each wafer assembly die.
42. (original) The method of claim 1, wherein the plurality of deflectable elements are formed on or in the second wafer.
43. (original) The method of claim 42, wherein when the first and second wafers are aligned and bonded together, the deflectable elements on the second wafer are each disposed proximate to a corresponding electrode on the first wafer.
44. (original) The method of claim 1, further comprising packaging the wafer assembly dies.
45. (original) The method of claim 1, wherein the deflectable elements are micromirrors having jagged or zig-zag edges.

46. (original) The method of claim 1, further comprising applying a stiction reducing agent to one or both wafers before or after bonding the two wafers together, but before singulating the wafer assembly into dies.
47. (original) The method of claim 1, further comprising applying a getter to one or both wafers before bonding the two wafers together into a wafer assembly.
48. (original) The method of claim 47, wherein the getter is a molecular, hydrogen and/or particle getter.
49. (original) The method of claim 47, wherein the getter is a particulate and moisture getter.
50. (original) The method of claim 47, wherein the getter is capable of absorbing moisture.
51. (original) The method of claim 46, wherein the stiction reducing agent is a silane applied to the deflectable elements.
52. (original) The method of claim 46, wherein the stiction reducing agent is a chlorosilane.
53. (original) The method of claim 1, further comprising aligning the wafers prior to bonding.
54. (original) The method of claim 53, wherein the aligning of the wafers has an accuracy of 10 microns or less.
55. (original) The method of claim 54, wherein the accuracy is 3 micron or less.

56. (original) A method for forming a MEMS device, comprising:
- providing a first wafer;
 - providing a second wafer;
 - providing a sacrificial layer on or in the first or second wafer;
 - forming a plurality of MEMS elements on the sacrificial layer;
 - releasing the plurality of MEMS devices by etching away the sacrificial layer;
 - mixing one or more spacer elements into an adhesive or providing one or more spacer elements separately from the adhesive for separating the wafers during and after bonding;
 - applying the adhesive to one or both of the first and second wafers;
 - bonding the first and second wafers together with the spacer elements therebetween so that the first and second wafers are held together in a spaced apart relationship as a wafer assembly; and
 - singulating the wafer assembly into individual dies.
57. (original) The method of claim 56, wherein the releasing comprises providing an etchant selected from an interhalogen, a noble gas fluoride, a vapor phase acid, or a gas solvent.
58. (original) The method of claim 57, wherein the releasing is followed by a stiction treatment.
59. (original) The method of claim 58, wherein the stiction treatment comprises treatment with a silane.
60. (original) The method of claim 58, wherein the stiction treatment is followed by said bonding.

61. (original) The method of claim 60, wherein the time from releasing to bonding is less than 6 hours.
62. (original) The method of claim 56, wherein the first wafer is an optically transmissive wafer or a wafer having one or more layers that when removed result in an optically transmissive substrate.
63. (original) The method of claim 62, wherein the first wafer is glass, borosilicate, tempered glass, quartz or sapphire.
64. (original) The method of claim 56, wherein the second wafer is a dielectric or semiconductor wafer.
65. (original) The method of claim 64, wherein the second wafer comprises GaAs or silicon.
66. (original) The method of claim 56, wherein the first and second wafers are bonded together with an adhesive.
67. (original) The method of claim 66, wherein the adhesive is an epoxy.
68. (original) The method of claim 67, wherein the epoxy comprises balls or rods of predetermined diameter.
69. (original) The method of claim 56, wherein the wafer assembly is separated into individual dies by scribing and breaking.
70. (original) The method of claim 56, wherein the wafer assembly is tested for abnormalities prior to separation into the individual dies.
71. (original) The method of claim 56, further comprising providing a spacing wafer between the first and second wafers.

72. (original) The method of claim 56, further comprising providing microfabricated spacers on one or both of the first and second wafers prior to bonding.
73. (original) The method of claim 66, wherein the adhesive is dispensed by automated controlled liquid dispensing through a syringe.
74. (original) The method of claim 66, wherein the adhesive is dispensed by screen, offset or roller printing.
75. (original) The method of claim 73, wherein the syringe is moved along X-Y coordinates for dispensing.
76. (original) The method of claim 56, wherein the aligning comprises registration of substrate fiducials on opposite wafers.
77. (original) The method of claim 76, wherein the registration is accomplished with a video camera having lens magnification.
78. (original) The method of claim 64, wherein the second wafer is a glass or quartz wafer.
79. (original) The method of claim 56, wherein the bonding of the wafers comprises the dispensing of a UV or thermal cure epoxy.
80. (original) The method of claim 79, wherein the bonding further comprises application of a force of 10 kg force or more.
81. (original) The method of claim 56, wherein the aligning comprises aligning each deflectable element on the first wafer with at least one electrode on the second wafer.

82. (original) The method of claim 56, wherein the separation of the wafer assembly comprises forming scribes on the first and second wafers.
83. (original) The method of claim 25, wherein the scribes are placed in an offset relationship to each other in at least one direction.
84. (original) The method of claim 25, wherein the separation further comprises breaking the wafer assembly along the scribe lines with a guillotine or fulcrum breaking machine.
85. (original) The method of claim 56, wherein the separation of the wafer assembly comprises sawing partially through each wafer followed by breaking along the sawed lines.
86. (original) The method of claim 56, wherein the sawing is done in the presence of a high-pressure jet of water.
87. (original) The method of claim 56, wherein the bonding comprises applying a sealant near the perimeter of each array on the wafer.
88. (original) The method of claim 87, further comprising applying a sealant around the perimeter of at least one of the wafers.
89. (original) The method of claim 56, wherein the bonding comprises applying an adhesive and spacers, the spacers having a size of from 1 to 100 microns.
90. (original) The method of claim 89, wherein the spacers have a size of from 1 to 20 microns.

91. (original) The method of claim 56, wherein the plurality of deflectable elements are reflective mirror elements and are formed on the second wafer which is a light transmissive wafer, at least with any surface coating removed therefrom.
92. (original) The method of claim 15, wherein the microfabricated spacers comprise an organic material.
93. (original) The method of claim 89, wherein the spacers are glass or plastic spacers.
94. (original) The method of claim 56, wherein the plurality of MEMS devices are optical or radio frequency switches, or are pressure or acceleration sensors.
95. (original) The method of claim 56, further comprising packaging the wafer assembly dies.
96. (original) The method of claim 56, wherein the MEMS devices are an array of micromirrors.
97. (original) The method of claim 56, further comprising applying a stiction reducing agent to one or both wafers before or after bonding the two wafers together, but before singulating the wafer assembly into dies.
98. (original) The method of claim 56, further comprising applying a getter to one or both wafers before bonding the two wafers together into a wafer assembly.
99. (original) The method of claim 98, wherein the getter is a molecular, hydrogen and/or particle getter.

100. (original) The method of claim 99, wherein the getter is a particulate and moisture getter.
101. (original) The method of claim 99, wherein the getter is capable of absorbing moisture.
102. (original) The method of claim 97, wherein the stiction reducing agent is a silane applied to the deflectable elements.
103. (original) The method of claim 97, wherein the stiction reducing agent is a chlorosilane.
104. (original) The method of claim 96, wherein a plurality of light blocking masks are formed on the second wafer.
105. (original) The method of claim 104, wherein when the wafer assembly is singulated into wafer assembly dies, a light blocking mask is disposed on a second wafer portion within each wafer assembly die.
106. (original) A method for making a MEMS device, comprising:
- providing a first wafer;
 - providing a second wafer;
 - forming circuitry and a plurality of electrodes on or in the first wafer;
 - forming a plurality of deflectable elements on or in either the first or second wafer;
 - applying an adhesion reducing agent and/or a getter to one or both of the wafers;
 - aligning the first and second wafers;
 - bonding the first and second wafers together to form a wafer assembly; and
 - separating the wafer assembly into individual wafer assembly dies.

107. (original) The method of claim 106, wherein a stiction reducing agent is applied to one or both wafers before or after bonding the two wafers together, but before singulating the wafer assembly into dies.
108. (original) The method of claim 107, wherein the stiction reducing agent is applied to at least one of the wafers prior to wafer bonding.
109. (original) The method of claim 106, wherein a getter is applied to one or both wafers before bonding the two wafers together.
110. (original) The method of claim 109, wherein the getter is a molecular, hydrogen and/or particle getter.
111. (original) The method of claim 110, wherein the getter is a particulate and moisture getter.
112. (original) The method of claim 110, wherein the getter is capable of absorbing moisture.
113. (original) The method of claim 107, wherein the stiction reducing agent is a silane applied in a vapor phase to the deflectable elements.
114. (original) The method of claim 113, wherein the stiction reducing agent is a chlorosilane.
115. (original) The method of claim 114, wherein the chlorosilane is a partially or fully fluorinated chlorosilane.
116. (original) The method of claim 115, wherein the chlorosilane has an alkyl chain of at least 8 carbons or a ring structure.

117. (original) The method of claim 116, wherein the chlorosilane is a trichlorosilane having an alkyl chain of at least 8 carbon atoms.
118. (original) The method of claim 116, wherein the chlorosilane is a trichlorosilane having a single or multi ring organic substituent.
119. (original) The method of claim 106, wherein one of the wafers is a glass or quartz wafer having one or more rectangular masks thereon.
120. (original) The method of claim 119, wherein one of the wafers comprises an array of micromirrors and the other of the wafers is transmissive to visible light.
121. (original) The method of claim 120, wherein the wafer transmissive to visible light comprises one or more visible light blocking areas.
122. (original) The method of claim 121, wherein the visible light blocking areas are substantially rectangular.
123. (original) The method of claim 106, wherein when the wafer assembly is singulated into wafer assembly dies, a light blocking mask is disposed on a second wafer portion within each wafer assembly die.
124. (original) A method for making a MEMS device, comprising:
 providing a wafer;
 providing a plurality of substrates that are transmissive to visible light, each smaller than said wafer, each substrate having a frame portion that is not transmissive to visible light;
 forming circuitry and a plurality of electrodes on or in the wafer;
 forming a plurality of deflectable elements on or in the wafer;
 aligning the substrates with the wafer ;
 bonding the substrates and wafer together to form a wafer

assembly; and
separating the wafer assembly into individual wafer assembly dies.

125. (new) A method of forming a microstructure device, comprising:

providing a substrate having a first material, a second material, and a sacrificial material between the first and second materials, wherein said sacrificial material comprises silicon;

exposing the substrate to a vapor-phase etchant comprising XeF_2 , which removes at least a portion of the sacrificial material from between the first and second materials, to leave a surface of the first material spaced from a surface of the second material by a gap, the surface of the first material being defined as a first surface and the surface of the second material being defined as a second surface; and

exposing at least one of the first and second surfaces to vapor-phase alkylsilane-containing molecules to form a coating over at least one of the first and second surfaces.

126. (new) The method of claim 125, wherein the alkylsilane-containing molecules include an alkylsilane.

127. (new) The method of claim 125, wherein the first material is different from than the second material.

128. (new) The method of claim 126, wherein the alkylsilane is a alkylchlorosilane.

129. (new) The method of claim 126, wherein the alkylsilane is a alkyltrichlorosilane.

130. (new) The method of claim 126, wherein the alkylsilane is a octyldecyltrichlorosilane.

131. (new) The method of claim 126, wherein the alkylsilane is a perfluorodecyltrichlorosilane.

132. (new) A method of forming a microstructure device, comprising:

providing a substrate having a first material, a second material, and a sacrificial material between the first and second materials;

exposing the substrate to a vapor-phase etchant which removes at least a portion of the sacrificial material from between the first and second materials, to leave a surface of the first material spaced from a surface of the second material by a gap, the surface of the first material being defined as a first surface and the surface of the second material being defined as a second surface;

exposing said at least one of the first and second surfaces to steam; and

exposing at least one of the first and second surfaces to vapor-phase alkylsilane-containing molecules to form a coating over at least one of the first and second surfaces.